



This kit contains a mating connector and housing for wiring according to the interlock application. This connector mates to the 451x-QIVC card interlock connector (Figure 1).

CAUTION When wiring to the pins of the interlock connector, ensure that all signals are properly insulated from the connector shell or chassis. Failure to do so may result in shorting the +5V signal to ground, which will result in system damage.

Figure 1
 451x-QIVC card interlock connector

Table 1
 Interlock cable terminal designations

14-Pin cable connector	Signal description
1	+5VD (+5V digital supply)
2	DIAG – RX (not implemented)
3	DIAG – TX (not implemented)
4	Interlock Channel A Enable
5	Interlock Channel B Enable
6	Interlock Channel C Enable
7	Interlock Channel D Enable
8	D–GND (digital ground)
9-14	---

